

AMENDMENT(S) TO THE SPECIFICATION:

Kindly amend paragraph [0001] as follows:

--[0001] This invention is a continuation-in-part of U.S. Patent Application 10/217,117 titled CHANNEL ESTIMATION IN A MULTICARRIER RADIO RECEIVER, filed August 12, 2002, ~~Docket/Reference No. CISCO-5748~~, now U.S. Patent 7,161,896. The contents of U.S. Patent Application 10/217,117 ~~is~~ are incorporated herein by reference and is called the Parent Patent Application herein.

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Kindly amend paragraph [0010] as follows:

--[0001] [0010] Above-referenced and incorporated-by-reference U.S. Patent Application 10/217,117 (the Parent Patent Application, now U.S. Patent 7,161,896) describes a practical method that improves the quality of the channel estimates using the possibility that there may be correlation between the channels of the subcarriers.

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Kindly amend paragraph [0044] as follows:

-- [0044] One embodiment of how the channel estimator initially determines the channel responses is described in above-referenced related and incorporated-by-reference U.S. patent application 10/217,117 titled *CHANNEL ESTIMATION IN A MULTICARRIER RADIO RECEIVER* (the Parent Patent Application),), now U.S. Patent 7,161,896. More details also are provided below.

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Kindly amend paragraph [0057] as follows:

--[0057] U.S. Patent application 10/367, 010 to Ryan et al., titled SELECTING THE DATA RATE OF A WIRELESS NETWORK LINK ACCORDING TO A MEASURE OF ERROR VECTOR MAGNITUDE, now U.S. Patent 6,898,198, described a signal

quality measuring circuit that is used in one embodiment. Patent application 10/367,010 is incorporated herein by reference.

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Kindly amend paragraph [0077] as follows:

-- [0077] The Parent Patent Application (incorporated-by-reference U.S. Patent Application 10/217,117, now U.S. Patent 7,161,896) described many configurations of a smoother and initial channel calculator.

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Kindly amend paragraph [00157] as follows:

--[00157] Many of the details of implementation are omitted herein. See the Parent Patent Application (incorporated-by-reference U.S. Patent Application 10/217,117, now U.S. Patent 7,161,896) for such details. For example, the writing and reading of the memories occurs in particular orders. Furthermore, both serial and parallel architectures are possible for the initial determination of the function of the channel estimates. Furthermore, when smoothing is included, in some embodiments, not all of the subcarriers are smoothed.

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Kindly amend paragraph [00175] as follows:

-- [00175] Furthermore, while the invention has been described in the context of the Parent Patent Application (incorporated-by-reference U.S. Patent Application 10/217,117, now U.S. Patent 7,161,896) using smoothing filters that are each describable as a weighted moving average filter (some filters have equal coefficients), other embodiments may use one or more other smoothing filters. Many types of smoothing filters are known in the art.

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Kindly amend paragraph [00177] as follows:

--[00177] While one embodiment described in the Parent Patent Application
(incorporated-by-reference U.S. Patent Application 10/217,117, now U.S. Patent
7,161,896) uses a parallel-in, parallel out register to implement the smoothing filter, an
alternate embodiment uses a serial in, parallel out shift register.

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